

Tera Probe and PTI form joint venture

Yokohama, Japan and Hsinchu, TAIWAN – Tera Probe, Inc. (“Tera Probe”) one of the world’s largest wafer probing service companies today (Friday) signed MOU with Powertech Technology Inc. (“PTI”), on the establishment of a joint venture company (“JV”) in Hsinchu, Taiwan.

The current wafer test services of both Tera Probe and PTI have always been focusing on the field of Memory IC. To develop a new market opportunity for wafer test in the field of logic IC, the parties decide to form the JV to leverage on the synergy between Tera Probe’s expertise and PTI’s currently existing platform of wafer test and knowledge of the markets.

The JV is to be named “晶兆成科技” in Chinese, and “TeraPower Technology Inc.” in English. The JV will offer wafer test services for semiconductor products, including, but not limited to, DRAM, Flash Memory, SRAM, and logic semiconductor products. The annual turnover of the JV is expected to grow rapidly based on PTI’s current production capability.

The initial capital of the JV will be the value equivalent to twenty-five million US dollars (US\$25,000,000). The equity split between the joint venture parties will be 51% Tera Probe, and 49% PTI. The JV will be established by the end of August 2008 and operation is expected to commence in September 2008.

Commenting on the deal C.C. Liao, Deputy CEO & Spokesman of PTI, said: “PTI has become the world’s largest assembly and final testing services company on Memory products, but there is still a long distance for PTI to catch up with the top 4 of the assembly and test services companies around the world. The JV will be a more broadly based company with a stronger portfolio of product, providing PTI the opportunity to better capitalize on the good growth prospects in the assembly and final test market, especially for the logic semiconductor products, both in the Taiwan and overseas. Through the JV, the logic IC customers could also be provided the Turn-key services. In addition, the strategic investment enables a new growth opportunity for Tera Probe’s market share.”

Commenting on the deal Shigeru Koshimaru, President and CEO of Tera Probe, said: “Taiwan, the global center of wafer foundry business, is a quite attractive market for Tera Probe as a wafer probing service company. In addition to that, accompanying the best partner PTI, Tera Probe has decided to set up its first overseas subsidiary in Taiwan as a joint venture with PTI. Tera Probe expects the JV will contribute to the enhancement of our business both of memory and logic devices providing the Turn-key services with Tera Probe’s cutting edge technology of wafer probing and PTI’s performance in the assembly and final test business. We will provide our highly efficient wafer probing service at competitive prices to the global fabless companies and Japanese semiconductor manufacturers who use foundry services in Taiwan. We try and expect to expand our customers and business base not only in Japanese market, also worldwide.”

About Tera Probe, Inc.

Tera Probe was founded in 2005, Its headquarter is located in Yokohama, Japan. Tera Probe is now one of the largest wafer probing service companies in the world.

Tera Probe boasts both leading-edge technology and facilities for implementing memory wafer probing services for the Japan's leading DRAM manufacturers, and we provide the world's top level technology, know-how and mass-production capabilities for 300mm wafer probing. In addition to memories, we also exploit these high levels of technology, know-how and mass-production to concentrate on other elements, such as SoC and other semiconductor and we have also expanded the field of business into final tests.

Tera Probe has been engaged in development of the next-generation testing technology, such as multi parallel probing and wafer level burn-in (WLBI) and contributes to its global customers' business efficiency with such technologies.

www.teraprobe.com/english

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About Powertech Technology Inc.

Powertech Technology Inc. (PTI) was founded in 1997 with headquarters in Hsin-Chiu, Taiwan. PTI focuses on assembly and testing services on memory and logic semiconductor products.

PTI currently provides wafer testing and stacked-MCP, mold uBGA, wBGA, TFBGA, TSOP, micro SD card, assembly, testing and burn-in testing services for various memory and logic devices. Eyeing the great developing potential of consumer electronics, PTI also extends the scope of service from pure memory to Stacking, MCP, PoP, SiP package in order to satisfy the demand from the customers.

With strategic alliances and continuous improvement, PTI offers services of reliable quality and advanced technology to fulfill customers' demand within world class manufacturing facility in Hsin-Pu and Hu-Kou plants. Additional information about PTI is available at www.PTI.com.tw.

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